

100

ADAPTIVE COMPUTING ENGINE (ACE)

FIG. 1

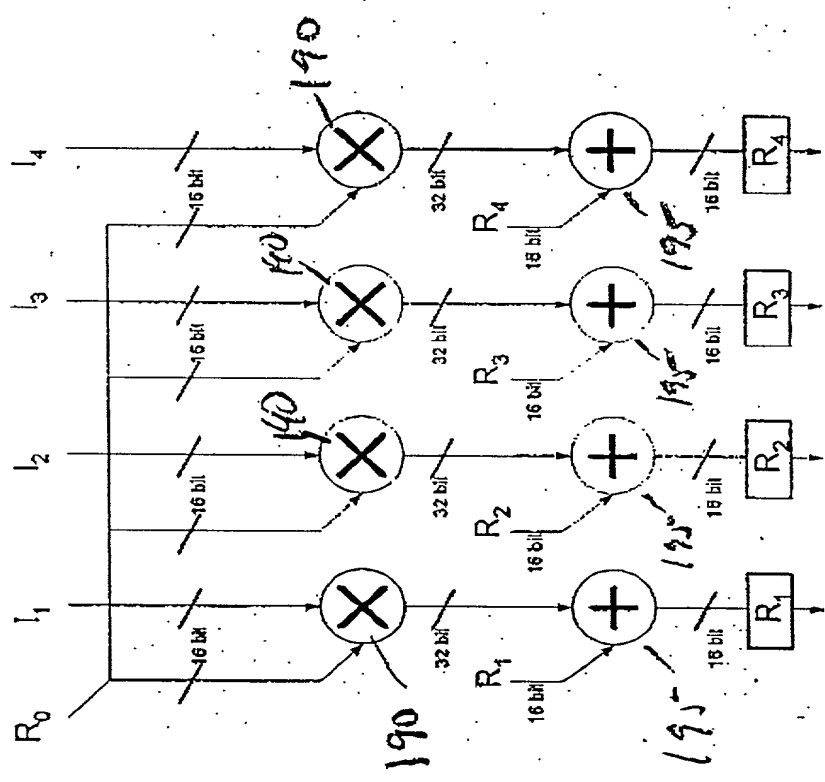


FIG. 2

TO OTHER MATRICES 150
(INCLUDING CONTROLLER 120 AND
MEMORY 140)

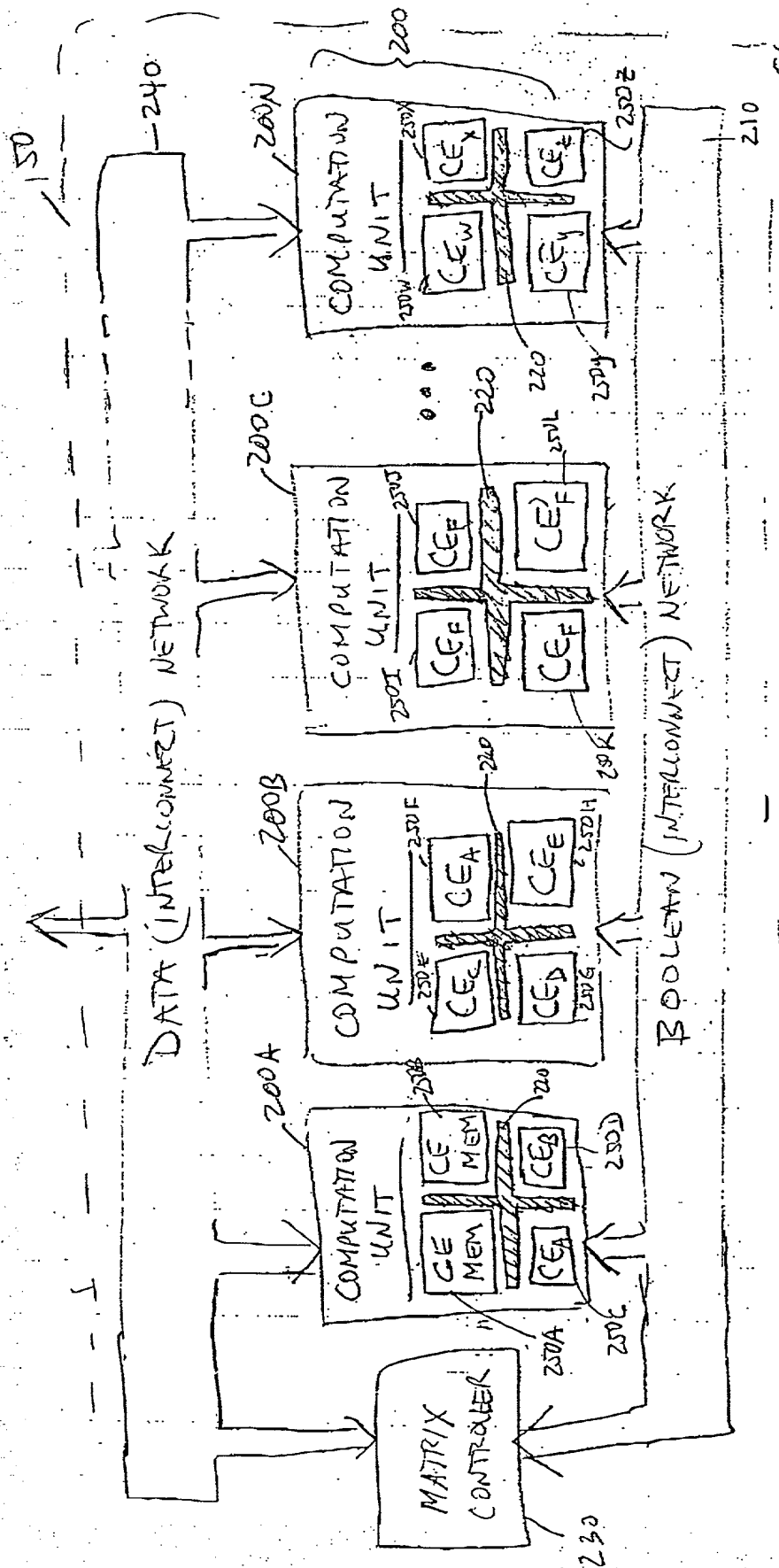


FIG. 3

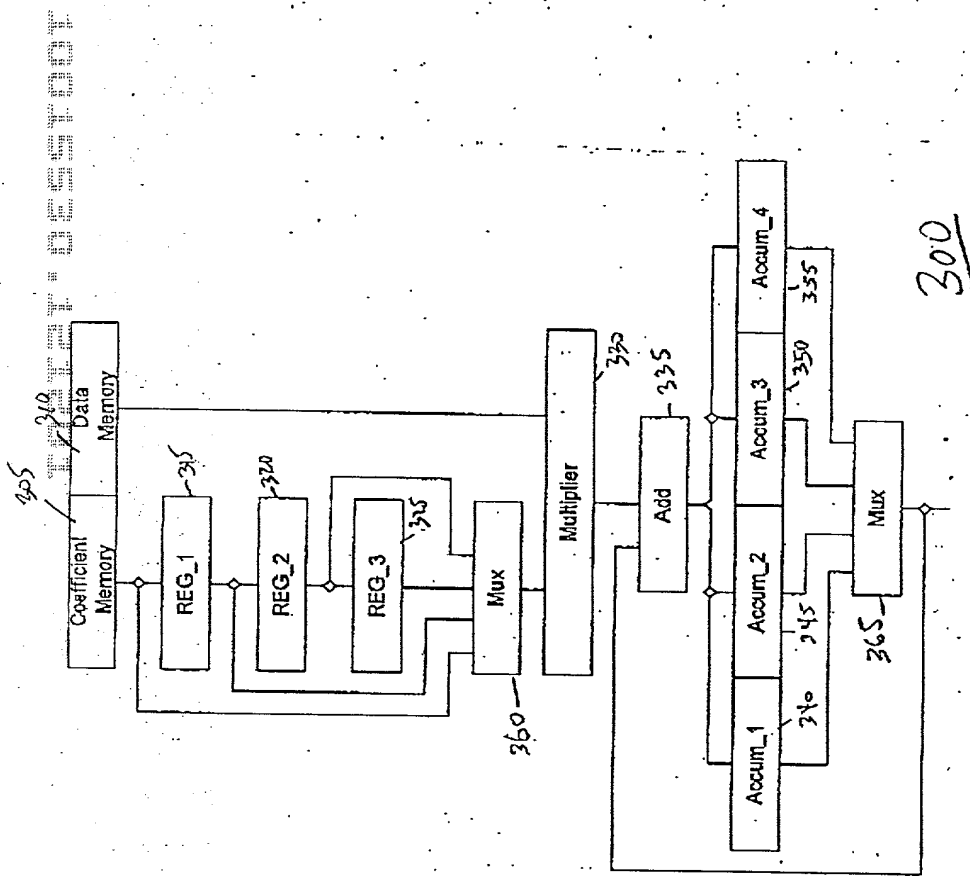
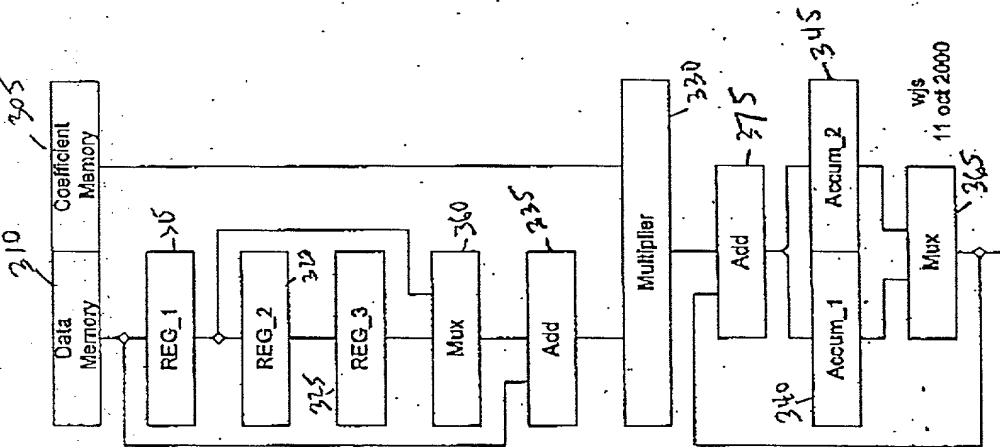


FIG. 5A

FIG. 5B



370

FIG. 5B

1. The present invention relates to a method and apparatus for processing data.

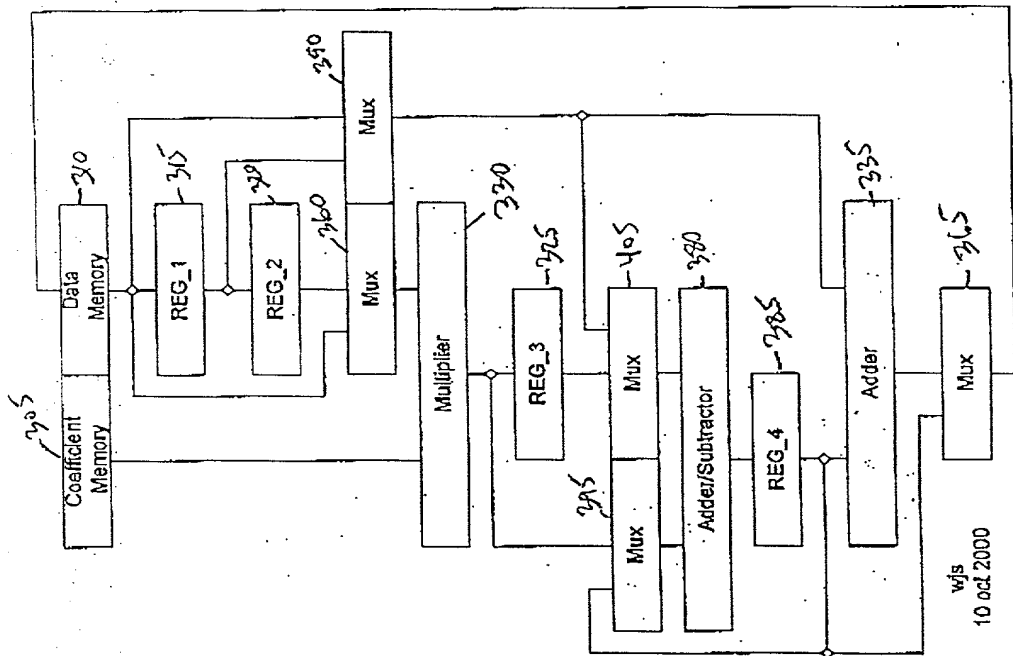
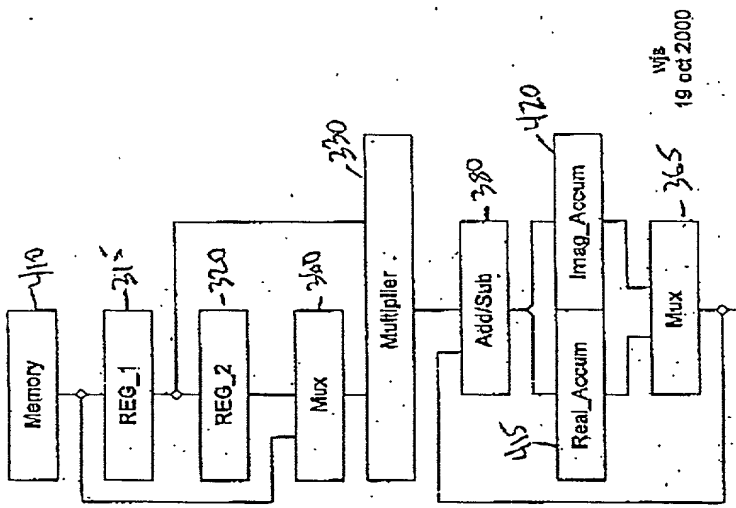


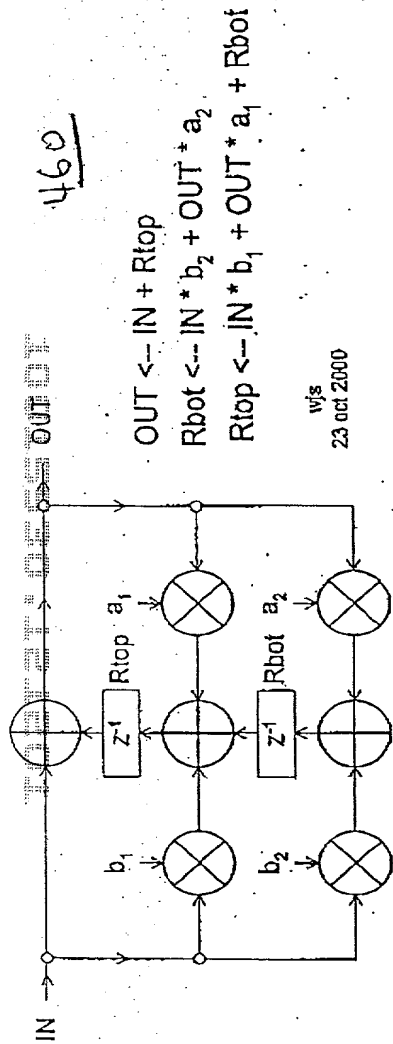
FIG. 5C

400



440

FIG. 5D



$$OUT \leftarrow IN + R_{top}$$

$$R_{bot} \leftarrow IN * b_2 + OUT * a_2$$

$$R_{top} \leftarrow IN * b_1 + OUT * a_1 + R_{bot}$$

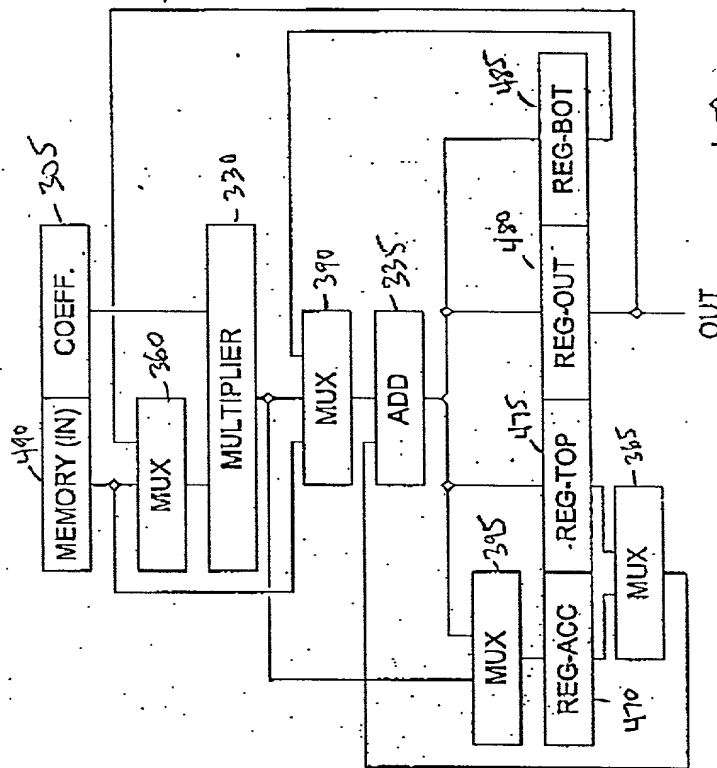


FIG. 5E

QUAD IN
PEER 3
PEER 2
PEER 1

505

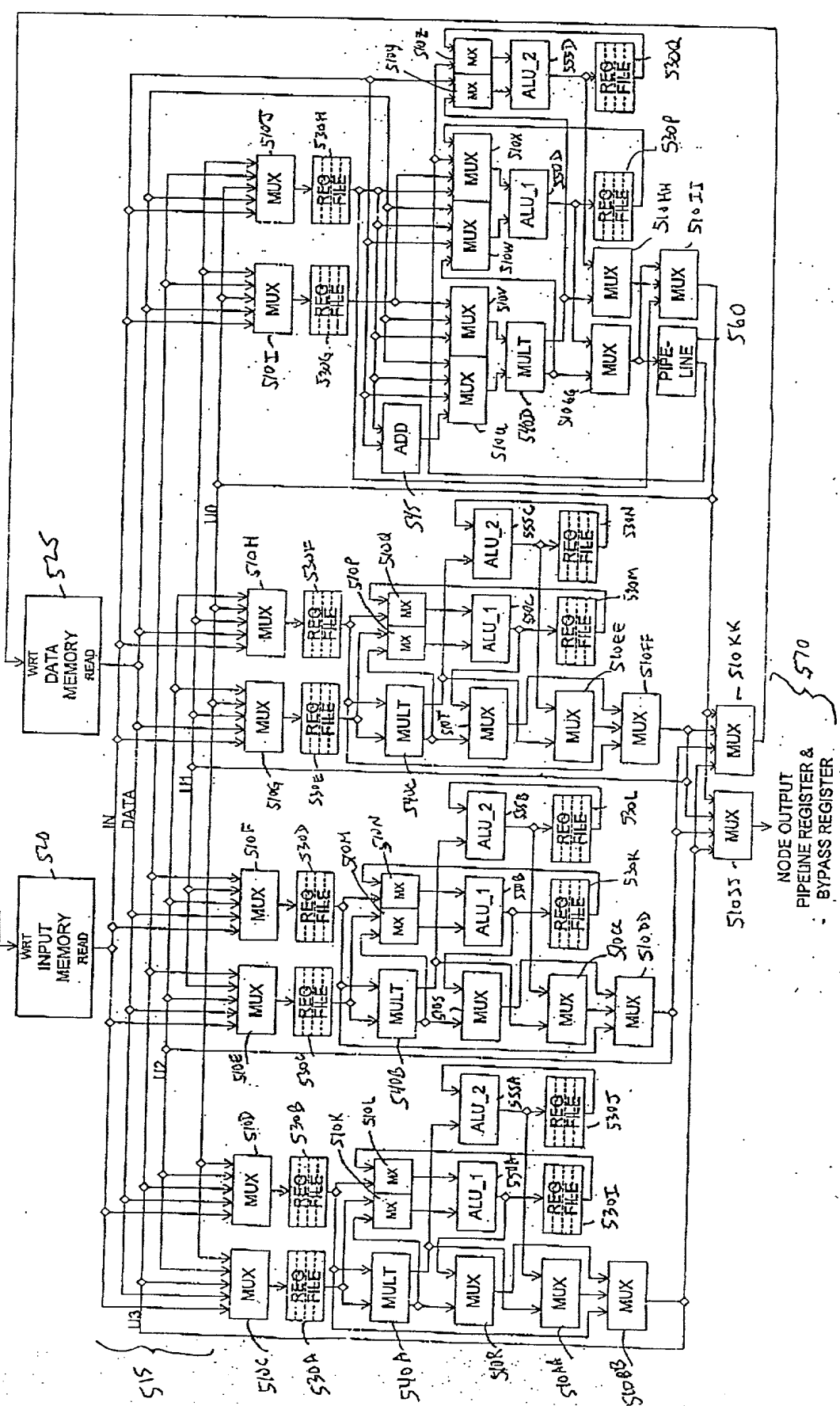
REAL TIME INPUT

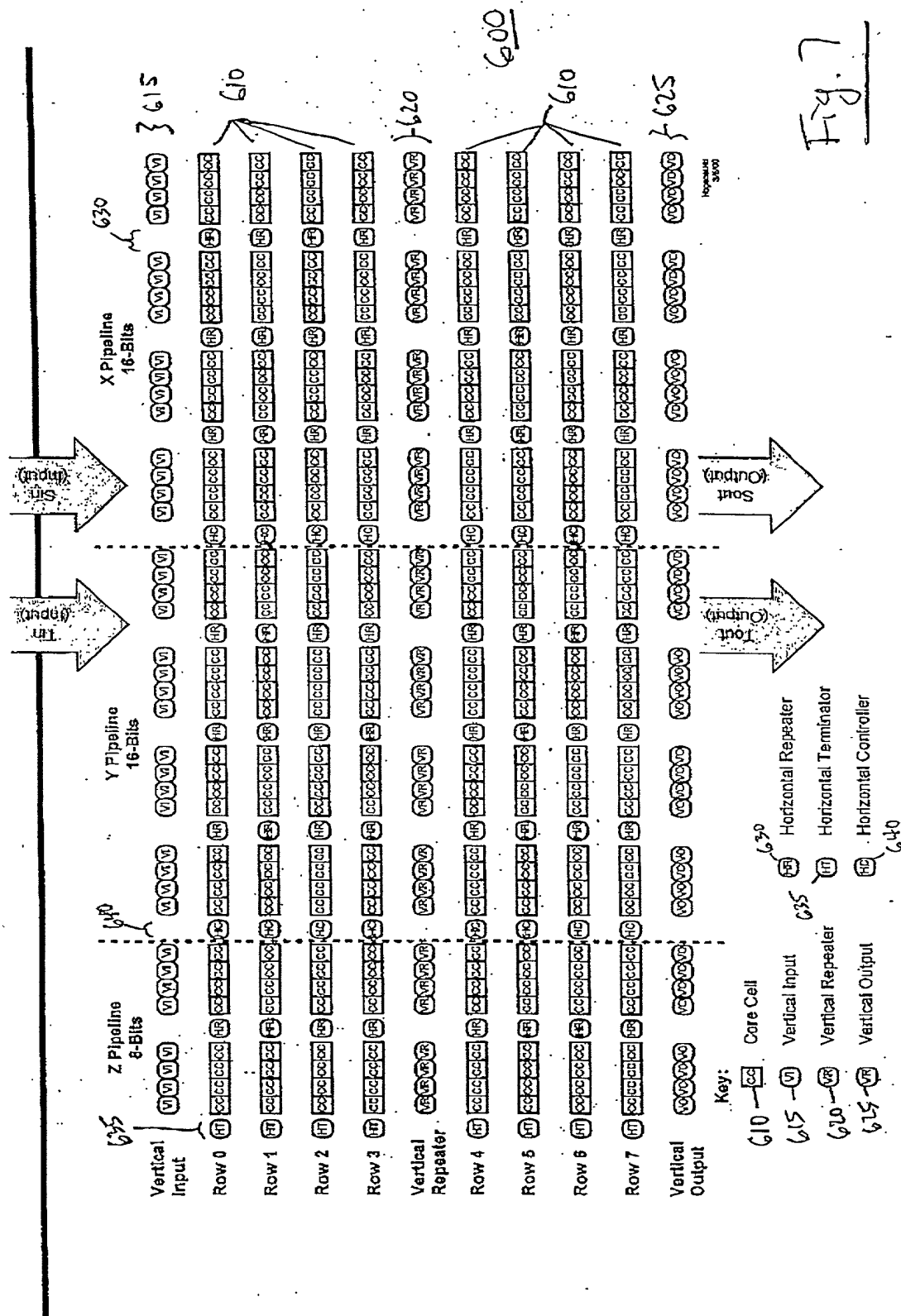
570A

570B

500

FIG. 6





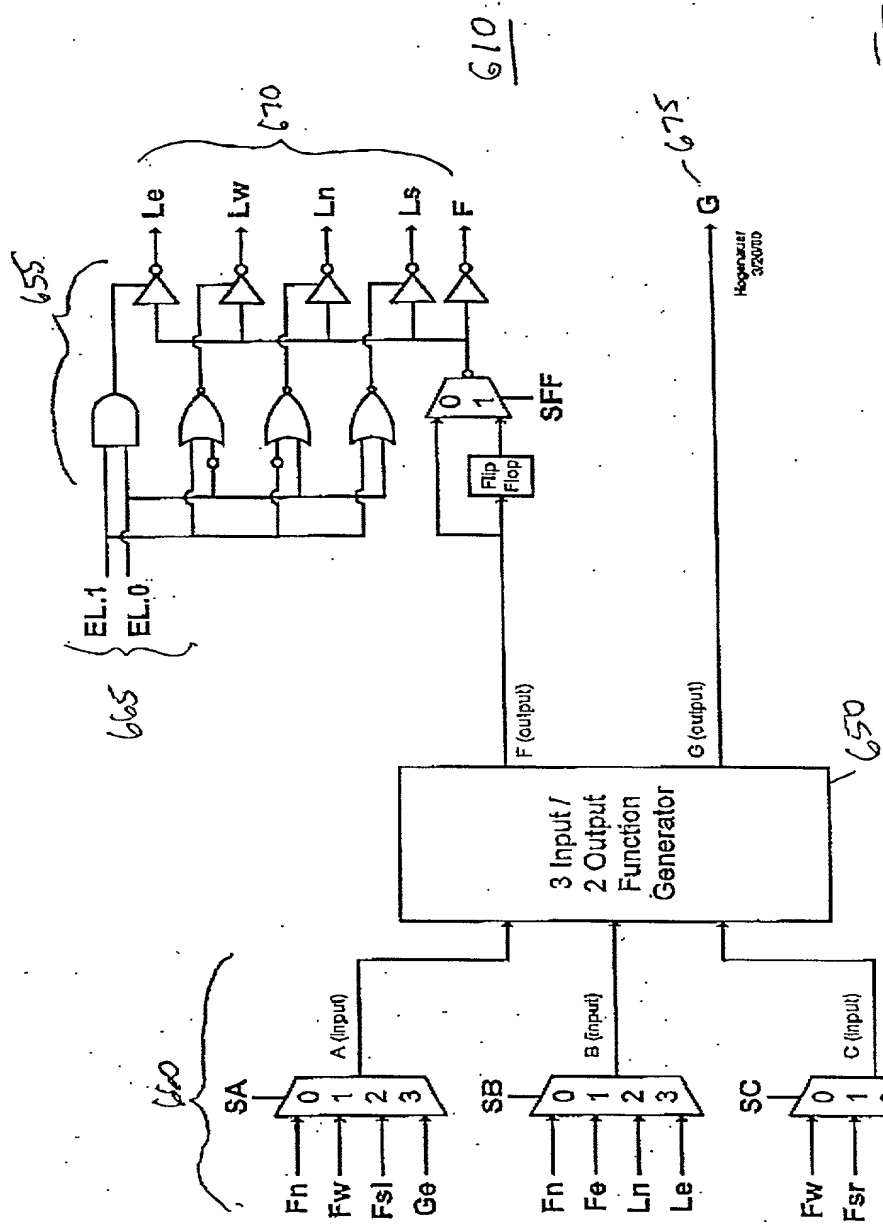
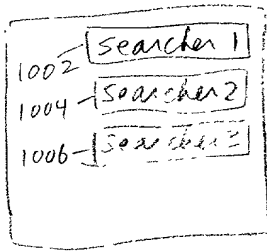


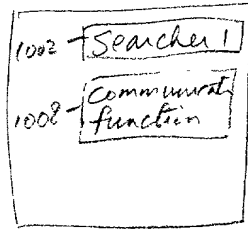
Fig. 8



Fig. 9

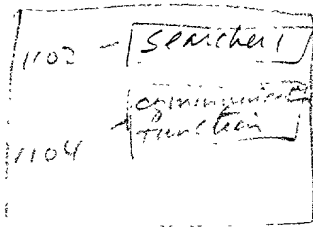


At power-up

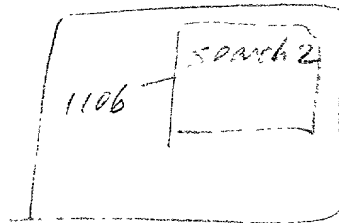


After system acquisition

FIG. 10

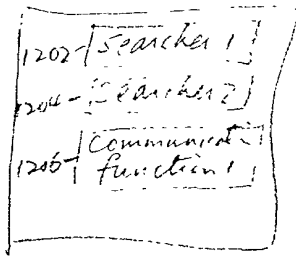


Before re-allocation

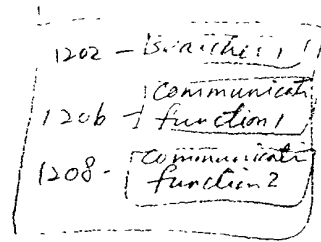


After re-allocation

FIG. 11



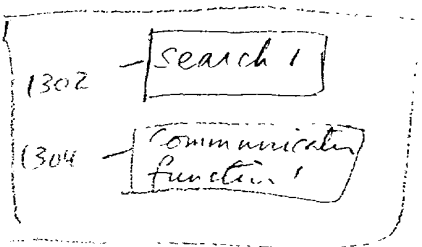
Before re-allocation



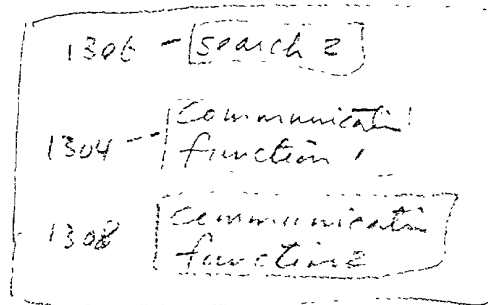
After re-allocation

FIG. 12

FIG. 12



Before re-allocation



After re-allocation

FIG. 13